

## Computer Architecture Exam Solutions

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### CSE 490/590 Computer Architecture Midterm Solution

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### EXAM 1 SOLUTIONS - Electrical and Computer Engineering

1. This is a closed book exam. You are allowed to have two letter-sized cheat sheets. 2. No electronic devices may be used. 3. This exam lasts 3 hours. 4. Clearly indicate your nal answer. 5. Please show your work when needed. 6. Please write your initials on every odd page.

### Name: SOLUTIONS - Electrical and Computer Engineering

Solution: We must first determine the miss rate of the L1 cache to use in the revised AMAT formula: AMAT = Hit Time + Miss Rate x Miss Penalty 2.4 = 1 + Miss Rate x 80 Miss Rate = 1.75% Next, we can calculate the target AMAT | i.e. AMAT with L2: Speedup = Time (old) / Time (new) 1.65 = 2.4 / Time (new)

### CSE 30321 | Computer Architecture | | Fall 2010 Final Exam

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### COMP 212 Computer Architecture Mid-term Exam Fall 2008

Computer Architecture 1 Fall 2011 Final Exam Solutions, Uppsala University Page 2 of 8 False/True&(6points)& Circle either false or true or neither. 0 points for no answer, -1 point for an incorrect answer, +1 point for a correct answer. 4a. For forwarding you need only look at the data available in the WB stage. False True False.

### Dark 1 HT2011 Exam Solutions - Uppsala University

Computer Architecture - Instructions 1. Write your name now. Name: Final Exam A simple (No wireless) calcul- 2. 3. 4. 5. 6. 8. Maximum score: 115. This exam is open book and open notes. Iator is allowed. You should have 6 questions in 16 pages. Exam length: 2 hours. Read all the questions before you begin. Show all your work. Write clearly.

### FINAL EXAM3 - Department of Electrical & Computer Engineering

CMSC411 Fall 2009 Final Exam Solution 1. (28 pts) Architectures a. Give an example of an area of computer architecture where bandwidth has improved faster than latency. How has this gap affected performance? Memory, storage, networks, etc. b. Describe how speculation can improve performance where dynamic scheduling cannot.

### CMSC411 Fall 2009 Final Exam Solution - UMD

Open book/notes (no electronic aids.) (Exam and solutions.) Virtual Memory. Reading: Essentials of Computer Architecture Chapter 11. Cache Technology. Reading: Essentials of Computer Architecture Chapter 12; Input/Output Concepts, Bus Architectures. Reading: Essentials of Computer Architecture Chapter 13.

### CS 250: Computer Architecture

2) How Computer Architecture is characterized? The computer architecture is characterized into three categories. System Design: It includes all the hardware component in the system, including data processor aside from the CPU like direct memory access and graphic processing unit; Instruction Set Architecture (ISA): It is the embedded ...

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